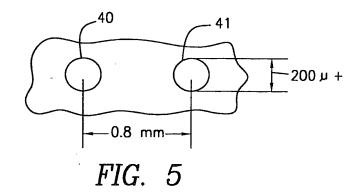
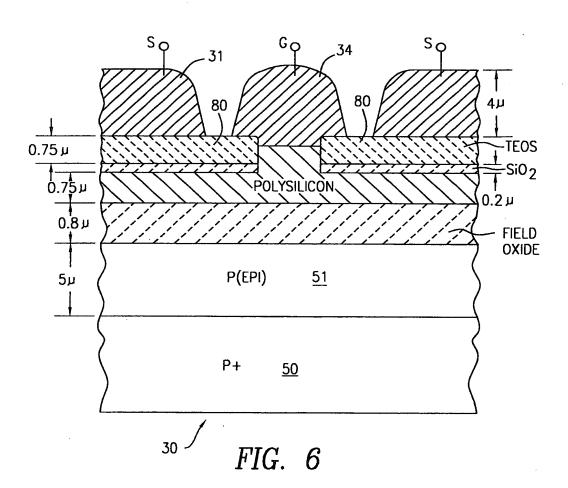
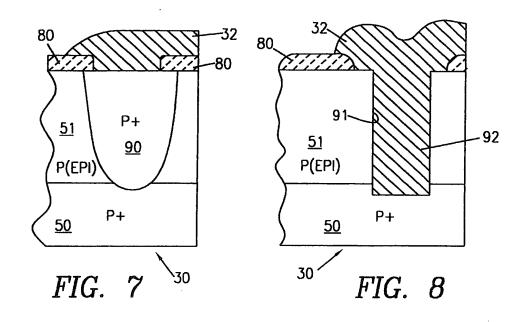


FIG. 4







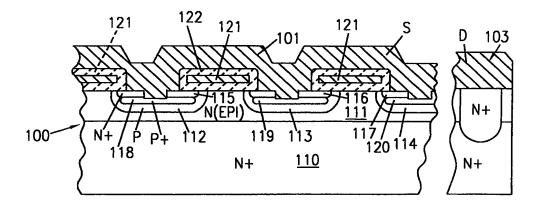


FIG. 11

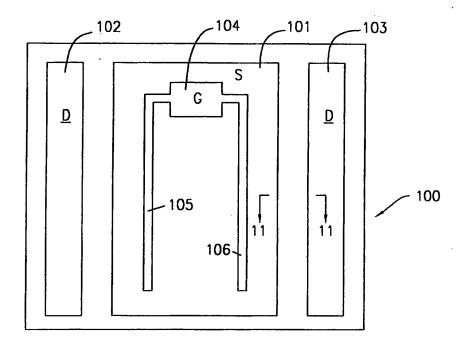


FIG. 9

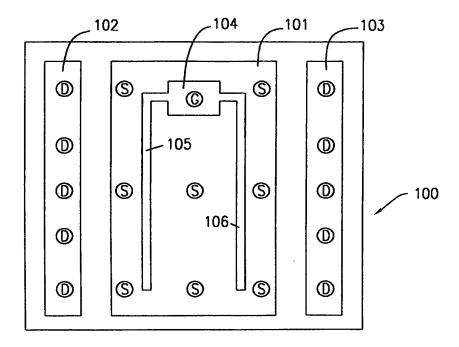
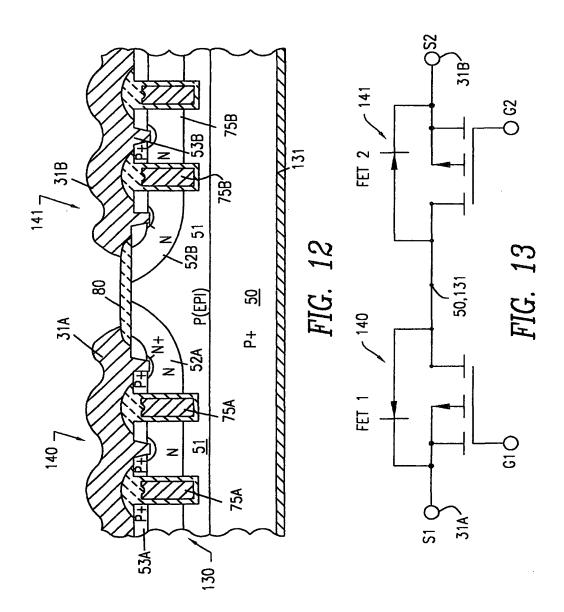
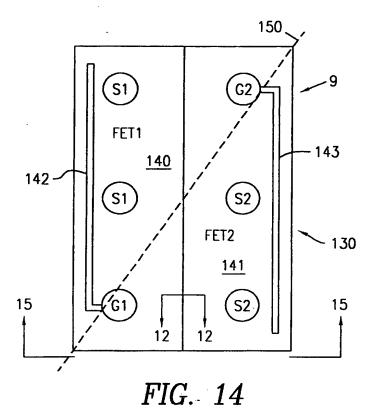
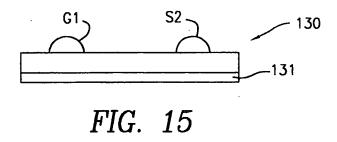


FIG. 10







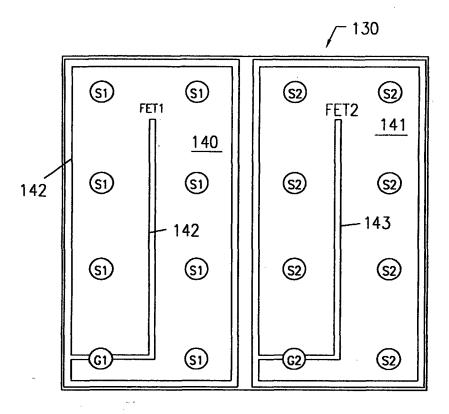
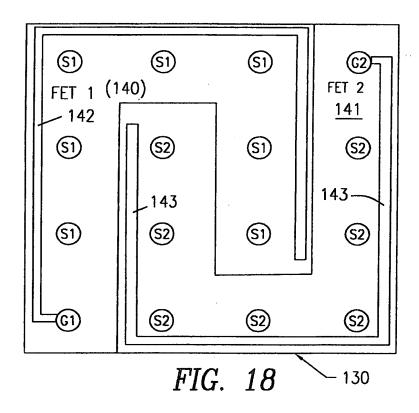


FIG. 16 130 **(S1) (S1) S2** (2) FET1 140 FET2 <u>141</u> <u>141</u> **(S1) (S1) S2 S2** -143 -142 **(S1) S2** (51) **S2 (S1)** <u>(1)</u> (32) **S2**

FIG. 17



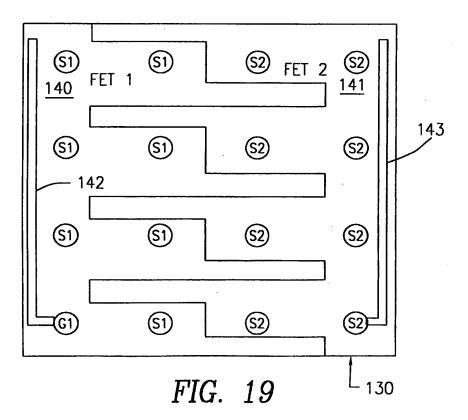


FIG. 20